

EXAMINER'S AMENDMENT

1. An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

Authorization for this examiner's amendment was given in a telephone interview with Michael Mallie on Feb. 9, 2009.

The application has been amended as follows:

Claim 1: line 3, insert --; determining a specified number of stored over-voltage conditions; indicating the specified number of stored over-voltage conditions; refraining from detecting the over-voltage input operating condition for a specified amount of time, and wherein the specified amount of time is associated with a power-on reset time --.

Claims 2-3 (Cancelled).

Claim 4: line 2, after "operational condition" insert --of an electronic circuit--, line 6 after "conditions" insert --indicating the specified number of said out-of-specification conditions; refraining from detecting the out-of-specification input operating condition for a specified amount of time, and wherein the specified amount of time is associated with a power-on reset time --.

Claims 6-7 (Cancelled).

Claim 9: (Cancelled).

Claim 10: change "9" to --4--.

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Claim 12: line 3, after “operational voltage” insert --of an electronic circuit--, line 5 delete “and”, line 6, after "over-voltage conditions" insert --; displaying said specified number of over-voltage conditions; and recording a clock speed at the time of occurrence of the over-voltage input operation condition--.

Claim 17: line 3, after “an electronic circuit” insert --; a display for displaying said selected number of out-of-specification input operational conditions; and a filter for refraining from detecting the out-of-specification input operating condition for a specified amount of time, wherein the specified of time is associated with a power-on reset time--.

Claim 22: line 3, after “input operational conditions” insert -- and a clock speed occurrence at the time of said conditions--, line 4 after "the electronic circuit" insert --to indicate said selected number--.

2. The following is an examiner’s statement of reasons for allowance: The present invention is directed to method/apparatus of storing out-of-specification condition of an electronic device. Each independent claim identifies the uniquely distinct features “storing data corresponding to input operating over-specification condition encountered by an electronic circuit in an indelible memory, determining a specified number of stored over-voltage conditions, displaying the specified number and refraining from detecting the out-of-voltage input operating condition for specified amount of time, wherein the specified of time is associated with a power-on reset time” in combination with the manner claimed. The closest prior arts, Chevallier [US 5,898,634], Mori et al. [US 6,891,214] and Perner [US 6,694,282] teach similarly inventions, but fail to teach the distinct limitations above. Therefore, either singularly or in combination, fail to anticipate or render the above underlined limitations obvious.

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Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Any inquiry concerning this communication or earlier communications from the examiner should be directed to SON M. TANG whose telephone number is (571)272-2962. The examiner can normally be reached on 5/8.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Daniel Wu can be reached on (571)272-2964. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

/ST/

/Benjamin C. Lee/
Supervisory Patent Examiner, Art Unit 2612